



2131

JLW

TRANSMITTAL FORM

(to be used for all correspondence after initial filing)

		Application No.	09/668,408
		Filing Date	September 22, 2000
		First Named Inventor	Carl M. Ellison
		Art Unit	2131
		Examiner Name	Taghi T. Arani
Total Number of Pages in This Submission	8	Attorney Docket Number	42390P8628X

ENCLOSURES (check all that apply)

<input checked="" type="checkbox"/> Fee Transmittal Form	<input type="checkbox"/> Drawing(s)	<input type="checkbox"/> After Allowance Communication to Group
<input type="checkbox"/> Fee Attached	<input type="checkbox"/> Licensing-related Papers	<input type="checkbox"/> Appeal Communication to Board of Appeals and Interferences
<input type="checkbox"/> Amendment / Response	<input type="checkbox"/> Petition	<input type="checkbox"/> Appeal Communication to Group (Appeal Notice, Brief, Reply Brief)
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<input type="checkbox"/> Affidavits/declaration(s)	<input type="checkbox"/> Power of Attorney, Revocation Change of Correspondence Address	<input type="checkbox"/> Status Letter
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<input type="checkbox"/> Declaration/POA		
<input type="checkbox"/> Response to Missing Parts under 37 CFR 1.52 or 1.53		
Remarks		

Cross reference to related application
return postcard
6 cited references

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm or Individual name	Gregory D. Caldwell, Reg. No. 39,926 BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP
Signature	
Date	September 21, 2004

CERTIFICATE OF MAILING/TRANSMISSION

I hereby certify that this correspondence is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Typed or printed name	Gayle M. Bekish		
Signature		Date	September 21, 2004

Based on PTO/SB/21 (04-04) as modified by Blakely, Sokoloff, Taylor & Zafman (wlr) 06/04/2004.
SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

The Patent Application of:)
Carl M. Ellison et al.)
Application No.: 09/668,408)
Docket No.: P8628X)
Filed: 9/22/00)
For: MANAGING A SECURE)
ENVIRONMENT USING A CHIPSET)
IN ISOLATED EXECUTION MODE)

Examiner: Taghi T. Arani

Art Unit: 2131

Phone No.: (703) 305-4274

CROSS REFERENCE TO RELATED APPLICATIONS

COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Applicants hereby cross-reference the following related patents and patent applications, which are all assigned to the same assignee as the present patent application.

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9/21/04

Date of Deposit

Gayle Bekish

Name of Person Mailing Correspondence

Gayle T.

Signature

9/21/04

Date

09/668,408

Reference No.	Application Serial No.	Patent No.	Title
1	09/672,603		Controlling Accesses To Isolated Memory Using A Memory Controller For Isolated Execution
2	09/822,986		Checking File Integrity Using Signature Generated In Isolated Execution
3	09/538,951		Platform And Method For Issuing And Certifying A Hardware-Protected Attestation Key
4	09/539,344		Managing A Secure Platform Using A Hierarchical Executive Architecture In Isolated Execution Mode
5	09/668,585		Managing A Secure Platform Using A Hierarchical Executive Architecture In Isolated Execution Mode
6	09/541,108		Platform And Method For Remote Attestation Of A Platform
7	09/540,612		Platform And Method For Generating And Utilizing A Protected Audit Log
8	09/540,946		Protecting Software Environment In Isolated Execution
9	09/668,610		Protecting Software Environment In Isolated Execution
10	09/538,954		Generating Isolated Bus Cycles For Isolated Execution
11	09/539,348	6,760,441	Generating A Key Hierarchy For Use In An Isolated Execution Environment
12	09/541,477	6,507,904	Isolated Instructions For Isolated Execution
13	09/540,611		Managing Accesses In A Processor For Isolated Execution
14	09/540,613		Managing A Secure Environment Using A Chipset In Isolated Execution Mode
15	09/668,408		Managing A Secure Environment Using A Chipset In Isolated Execution Mode
16	09/541,667		Attestation Key Memory Device And Bus
17	09/672,602		Attestation Key Memory Device And Bus
18	09/618,738	6,678,825	Controlling Access To Multiple Isolated Memories In An Isolated Execution Environment
19	09/618,489	6,633,963	Controlling Access To Multiple Memory Zones In An Isolated Execution Environment
20	10/683,542		Controlling Access To Multiple Memory Zones In An Isolated Execution Environment
21	09/618,659		Resetting A Processor In An Isolated Execution Environment
22	09/751,586		Resetting A Processor In An Isolated Execution Environment

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It may be noted that the above list includes the present application, and the above list partially overlaps the cross-reference to related applications that was filed earlier.

If there are any questions that may be cleared up in a telephone interview, the Examiner is invited to call the undersigned at (512) 732-3927.

Respectfully submitted,

Dated: 9/20/04



Michael R. Barré
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09/668,408

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:)
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For: MANAGING A SECURE)
ENVIRONMENT USING A CHIPSET)
IN ISOLATED EXECUTION MODE)

Examiner: Taghi T. Arani

Art Unit: 2131

Phone No.: (703) 305-4274

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. § 1.97

COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Enclosed please find an Information Disclosure Statement, which is being submitted after the mailing of the first Office Action but before the mailing of a Final Office Action. A copy of each cited reference is also enclosed.

In accordance with 37 C.F.R. § 1.97(e), I hereby state that no item of information in the Information Disclosure Statement filed herewith was cited in a communication from a foreign patent office in a counterpart foreign application, and, to my knowledge after making reasonable inquiry, no item of information contained in this Information Disclosure Statement was known to any individual designated in 37 CFR 1.56(c) more than 3 months prior to the filing of this Information Disclosure Statement.

The submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made in the subject

09/668,408

application, and is not to be construed as an admission that the information cited in this statement is material to patentability.

Applicants respectfully request that the Examiner consider the cited reference(s), initial each reference on the enclosed Information Disclosure Statement to indicate such consideration, and return a copy of the initialed Information Disclosure Statement to Applicants. If the Examiner has any questions, the Examiner is invited to contact the undersigned at (512) 732-3927.

Respectfully submitted,

Dated: 9/20/04

MR Barré

Michael R. Barré
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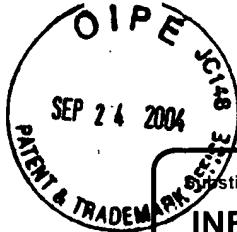
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SEP 24 2004

~~Substitute for form 1449A/PTO~~

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Sheet 1 of

<i>Complete if Known</i>	
Application Number	09/668,408
Filing Date	September 22, 2000
First Named Inventor	Carl M. Ellison
Art Unit	2131
Examiner Name	Taghi T. Arani
Attorney Docket Number	42390P8628X

U.S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

Examiner Signature	Date Considered
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*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.

¹Applicant's unique citation designation number (optional);See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04;Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3);²For Japanese patent documents, the indication of the year of reign of the Emperor must precede the serial number of the patent document.³Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible;Applicant is to place a check mark here if English language Translation is attached.

Based on PTO/SB/08A (08-03) as modified by Blakely, Solokoff, Taylor & Zafman (wlr) 08/11/2003.

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